

Senior Design Bi-Weekly Status Report; Fall 2020

Team Name: sdmay21-14

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Report Period: Sept 28-Oct 11

Summary of Progress in this Period

- Finalized scope of project
- Created an initial timeline for the project
- Decided on some parts we will be using for our design
- Finalized some details and a very high level schematic of the design
- Created a Git repository
- Started a project in Altium
- Started creating footprints for components
- Got information from Prof. Duwe's grad student on operating values for harvesting
- Completed first draft of design document section 1 and 2

Pending Issues

- Get response from Powercast to see if we can get more information on power harvester IC, hysteresis, etc.
- Decide on what the wake up trigger will be

Plans for Upcoming Reporting Period

- Finalize the high level schematic
- Have all components spec'd and in a library for Altium
- Decide on physical data/test to prove feasibility by the end of the semester